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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/065,921	11/29/2002	Michael Jacob	2002P09632US	5322
31366	7590	09/21/2004	EXAMINER	
HORIZON IP PTE LTD 166 Kallang Way 6th Floor SINGAPORE 349249, SINGAPORE			LE, TOAN K	
			ART UNIT	PAPER NUMBER
			2824	

DATE MAILED: 09/21/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/065,921

Applicant(s)

JACOB ET AL.

Examiner

Toan Le

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-43 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-43 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 November 2002 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 9/10/2004.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☒ Other: East search history.

DETAILED ACTION

Information Disclosure Statement

1. This office acknowledge receipt of the following items from the Applicant:
Information Disclosure Statement (IDS) filed on September 10, 2004.
2. Information disclosed and list on PTO 1449 was considered.

Drawings

3. Figure 1 should be designated by a legend such as **--Prior Art--** because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Specification

4. Claims 37-43 are objected to because of the following informalities:
In claim 37: the term "he method" should be **--The method--**.
In claims 38-39: the term "3he method" should be **--The method--**.
In claims 40-43: the term "4he method" should be **--The method--**.
Appropriate correction is required.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

6. Claims 1-43 are rejected under 35 U.S.C. 102(e) as being anticipated by Roehr et al. (US. 2004/0057275 A1).

Roehr et al. disclose in Figures 3 and 4, a method of operating memory array comprising: providing a memory array including a plurality ferroelectric memory cells arranged in a plurality of columns and interconnected by wordlines (304 or WL), bitlines (BL, /BL or 407a-b), and platelines (306a-b or 406a-b), a column comprises a bitline pair having first and second bitlines (407a and 407b of fig. 4) coupled to a sense amplifier (409 and see paragraph [0017]); performing a memory access (read or write access) to the array, the access selects one of the columns of the memory cells (see paragraph [0020]); and a plateline decoder (see lines 9-12 of paragraph [0016]) providing a plateline pulse to the selected column (see paragraph [0004]), wherein the plateline pulse is equal to logic 1 or logic 0 (see paragraph [0006]).

Roehr et al. further disclose the memory cells being arranged in an open bitline or a folded bitline (figs 3, 4 and see paragraphs [0015], [0021]); unselected bitlines being set to a define state as logic 1 or logic 0 or a reference voltage which is equal about $V_{dd}/2$

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(see figs. 2, 5 and paragraphs [0007], [0023]), wherein the unselected bitlines are floated as isolating them from the sense amplifier by transistors (432 a-b of fig. 4). Also, Fig. 4 discloses the memory access select x columns of memory cell where x is greater than 1, and n adjacent columns of the selected column being unselected where n is equal to at least 1 (see fig. 4 and paragraphs [0017]-[0018]); platelines pulsing to the x selected columns (see paragraph [0019]).

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Hoya et al. (U.S. 6,493,251), Koo (U.S. 6,236,588), Ashikaga (U.S. 6,411,540), Komatsuzaki (U.S. 6,574,135), Takeuchi et al. (U.S. 6,366,490), Honigschmid et al. (U.S. 2002/0054501), Ihara (U.S. 5,517,446) disclose an integrated circuit similar to that of Roehr et al.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Le whose telephone number is (571) 272-1872. The examiner can normally be reached on M-F (8.00AM - 5.30PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TL
September 13, 2004



MICHAEL S. LEBENTRITT
PRIMARY EXAMINER